ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention

MODULAR DLL ARCHITECTURE FOR GENERATING MULTIPLE TIMINGS

Application Number :

Confirmation Number:

First Named Applicant: Darren Anand

Attorney Docket Number: BUR920030161US1

Art Unit: Examiner:

Search string: (6147527 or 6310822 or 6374360 or 0064082 or 6424592).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
|------|----------|------------|------------|-------------|------|-------|----------|
| | 1 | 6147527 | 2000-11-14 | Lee et al | | | |
| | 2 | 6310822 | 2001-10-30 | Shen | | | |
| | 3 | 6374360 | 2002-04-16 | Keeth et al | | | |
| | 4 | 0064082 | 2002-05-30 | Maruyama | | | |
| | 5 | 6424592 | 2002-07-23 | Maruyama | | | |

Signature

| Examiner Name | Date |
|---------------|------|
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